

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Terunao HANAOKA et al.

Application No.: 09/700,464

Filed: November 15, 2000

For: SEMICONDUCTOR DEVICE AND METHOD OF
 MANUFACTURING THEREOF, CIRCUIT
 BOARD, AND ELECTRONIC INSTRUMENT



Group Art Unit: 2826

Examiner: L. ANDUJAR

Docket No.: 107284

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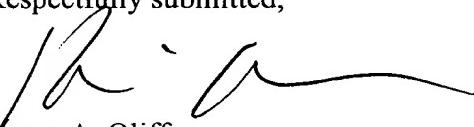
Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. §1.114, Applicants hereby request continued examination and entry and consideration of the May 27, 2003 submission. The above-identified application was filed on or after June 8, 1995. Thus, entry is proper under 37 C.F.R. §1.114(d).

Attached hereto is our check no. 143459 in the amount of \$750.00 as payment of the fees set forth in 37 C.F.R. §1.17(e). The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this page are enclosed.

Respectfully submitted,


 James A. Oliff
 Registration No. 27,075

Kevin M. McKinley
 Registration No. 43,794

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750.00 OP

JAO:KMM/jfl

Date: June 24, 2003

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